VNR10C209-INV

PRODUCT INFORMATION

All information is subject to change without notice. Please read bottom notes.

TENTATIVE

MECHANICAL SPECIFICATIONS

Item	Specifications
Dimensional Outline	32.5(W)×126.5(H)×12.0max.(D) (mm)
Applicable TFT Module	LTM10C209A, LTM10C209H, LTM10C273
Weight	38g(max.)

ABSOLUTE MAXIMUM RATINGS *1

Item	Symbol	Min.	Max.	Unit
Supply Voltage	V_{IN}	0	20.0	V
Input Voltage	V _{ON/OFF}	-1	V _{IN} +1	V
Storage Temp. *2	$T_{ m stg}$	-20	60	°C
Operating Temp. *2	T_{op}	0	50	°C
Humidity ²	Н	10	90	%(RH)

^{*1 :} Do not exceed the maximum rating values under the worst probable conditions taking into account the supply voltage variation, input voltage variation, variation in part constants, ambiemt temperature and so on. Otherwise the inverter unit may be damaged.

ELECTRICAL SPECIFICATION*3

Item		Symbol	Min.	Тур.	Max.	Unit
Supply Voltage		V_{IN}	9.0	12.0	19.0	V
Input Current (V _{IN} =12V)		<i>I</i> _{IN}	570	670	770	mA
Open Output Voltage		V_{OPEN}	1500	ı	1	V(0-p)
Frequency		f	25	30	35	kHz
Rush Current (V _{IN} =12V)		I RUSH	1	1	8.0	A(o-p)
Leak Current (V _{IN} =12V)		I LEAK	-	-	10	μΑ
Output Current	Volume Max.	I _{FL}	5.5	6.0	6.5	mA(rms)
	Volume Min.		2.0	2.5	3.0	
V _{ON/OFF} Signal Voltage (V _{IN} =12V)		V_{ON}	-0.8	ı	0.6	V
		V _{OFF}	V_{IN} -0.3	-	V _{IN} +0.8	V
				open		

^{*3 :} The characteristics without remarks are measured on condition with $V_{\rm IN}$ =12V and VR-VR60:short.

^{*2 :} Wet bulb temperature should be 39 °C max.,and no condensation of water.

^{*}The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by Toshiba Matsushita Display Technology or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Toshiba Matsushita Display Technology or others.

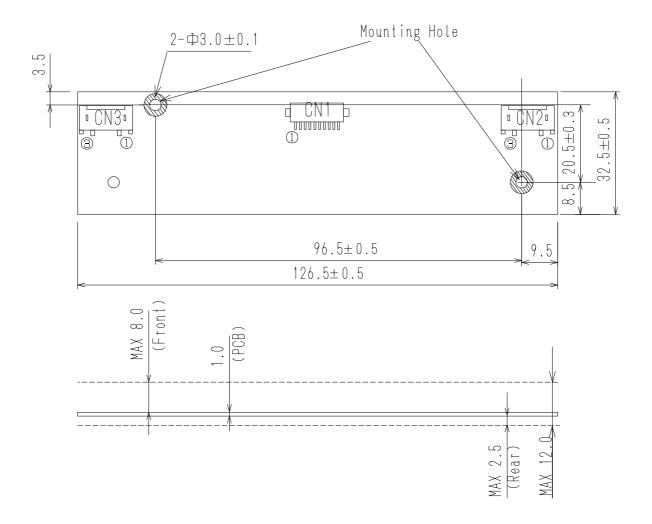
^{*}The information contained herein may be changed without prior notice. It is therefore advisable to contact Toshiba Matsushita Display Technology before proceeding with the design of equipment incorporating this product.

DIMENSIONAL OUTLINE

Unit: mm

Standard tolerance: 0.5

CN2,3 Pin No.1 High Voltage 3 Low Voltage

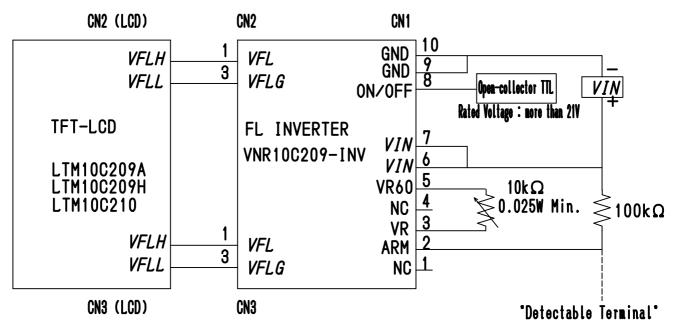


^{*4 :} Recommended screw for mounting is M2.5.

^{*5 :} Dimension without tolerance are reference purpose only.

^{*6 :} When fixing this inverter, do not use any holes other than the mounting holes shown above.

SCHEMATIC DIAGRAM OF INVERTER CONNECTION



CONNECTOR PIN ASSIGNMENT FOR INTERFACE

CN1: DF13-10P-1.25 (HIROSE ELECTRIC CO.,LTD.)

[Mating Connector : DF13-10S-1.25C (HIROSE ELECTRIC CO.,LTD.)]

Pin No.	Symbol	Function
1 *7	NC	No Connection
2 *8	ALM	Alarm signal(output) 0 to 0.4V :Normal operation
		High impedance : Non operation
3 *9	VR	Connect the Potentiometer (10k Ω) between VR and VR60
4 *7	NC	No Connection
5 ^{*9}	VR60	Connect the Potentiometer(10kΩ) between VR and VR60
6	VIN	Supply Voltage: 12V (9~19V)
7	VIN	Supply Voltage: 12V (9~19V)
8	ON/OFF	"L": FL ON, "H" or "Open": FL OFF
9	GND	OV
10	GND	OV

- *7 : Don't connect anything to the terminal of "NC".
 - If something connect to "NC" during operation, inverter may be damaged.
- *8 : This signal is an Alarm signal. Please connect resistor(100kΩ) between this signal line and VIN. So if FL stops operation by any trouble or its lifeend, this output signal becomes high impedance.
- *9 : Connect the Potentiometer($10k\Omega$) for Luminunce adjustment between "VR" and "VR60" terminal. 0 Ω : Luminance MAX.

10kΩ: Luminance MIN.

In case, electrical circuit or toggle switch adjust the luminance instead of potentiometer, please avoid VR-VR60 to open.

If VR-VR60 is open for any period of time, it is possible that inverter is not work normally.

CN2. CN3: SM02(8.0)B-BHS-1 (JAPAN SOLDERLESS TERMINAL MFG CO.,LTD.)

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Pin No.	Symbol	Function
1	V_{FL}	Output Power Supply for FL (High Voltage)
2	NC	No Connection
3	V_{FLG}	Output Power Supply for FL (Low Voltage)